SYLLABUS

Discipline name	Switching and Routing Systems				
Profile	Electronics and Telecommunications Engineering				
Specialization	Telecommunications Technologies and Systems				
Code	51324109				
Course leader	Professor Virgil Dobrota, Ph.D – virgil.dobrota@com.utcluj.ro				
Collaborators	Assistant Professor Tudor Blaga, Ph.D. – tudor.blaga@com.utcluj.ro,				
	Assistant Mihai Vancea – mihai.vancea@com.utcluj.ro				
Department	Communications				
Faculty	Electronics, Telecommunications and Information Technology				

Sem.	Type of discipline	Course	App	licati	ons	Course Applications Ind. study		AL	Credits	Form of assessment			
		[ho	hours/week] [hours/semester]			Cre							
			S	L	Р		S	L	Р		L	Ŭ	
6	Speciality	2	•	2	1	28	-	28	14	80	150	5	Exam

Acquired competences :

- To know the space-division and time-division switches
- To understand the principles of circuit switching and packet switching
- To get basic knowledge of queueing systems as major mathematical models for network design
- To know the principles of next generation networks from routing point of view

Acquired skills (what the student is able to do):

- To compare the performances of switching systems based on implementation complexity and blocking probability
- To evaluate the performances of a queuing system based on average waiting time and average number of clients
- To understand the routing principles and to determine the shortest path using routing algorithms and cost function
- Acquired abilities (what type of equipment/ instruments/ software the student is able to handle):
- To install and to configure an Asterisk IP-based PBX (Private Branch Exchange) running under Fedora Core 10 or Linux Live
- To implement a dialing plan and major functions of an Asterisk IP-based PBX

Prerequisites (if necessary):

Telephony, Statistical Mathematics, Excel calculus, C programming skills

urse/Lecture (course/lecture titles) Digital switching functions. Single stage space-division dwitch. Classification of multiple stage switches.
Digital switching functions. Single stage space-division dwitch. Classification of multiple stage switches
Aultiple stage switch with complete permutation paths (Clos).
trictly non-blocking three-stage Clos switch. Blocking probabilities. Lee's method. Generalization of Clos
ondition
Aultiple stage switch with single path (Banyan). Batcher-Banyan switches. Delta and Omega switches.
Aultiple stage switch with multiple path (Benes).
Time-Division Switching (T). Time-Space-Division Switching (TS, STS).
ime-Space-Division Switching (TST, TSSST). Examples of Telephone Exchanges and Switch Fabrics.
Digital Telephone Exchange Functions: BORSCHT
raffic Analysis. Characteristics of a Queueing System. Models for Clients Arrivals: Bernoulli, Poisson
$\Lambda/M/1/\infty$ System.
<i>I</i> /M/1/N System. M/M/m/∞ System. Erlang C Formula. M/M/m/m System. Erlang B Formula.
1/D/m/\infty and M/D/1/\infty Systems. M/G/1/\infty System. Pollaczek-Khinchin Formula. Traffic in
elecommunications Networks. Traffic Intensity. Traffic Processing.
IGN - Next Generation Network. Basics of Routing. Datagram-based routing. Virtual circuit-based routing.
Definitions: graph, arc, walk, path, cycle, connected graph, sub-graph, tree, spanning-tree. Packet
roadcasting methods: flooding, spanning-tree
Directed graph, directed arc, directed walk, directed path, arc distance, path length. Determination of the
hortest path based on Bellman-Ford algorithm.
Determination of the shortest path based on Dijkstra's algorithm.
Applications with Bellman-Ford and Dijkstra's algorithms.
Detimal routing. Link capacity. Latency. Link flow. Round-trip delay. Cost function. Minimizing the cost
unction. Randomization and metering methods.
Review. Examples of subjects given in the previous academic year

B. A	pplications – Laboratory (list of laboratories)
1	Linux Fedora Core 10 Installation
2	Introduction to Linux. Working with files. vi and joe editors.
3	Strictly Non-Blocking Three-Stage Clos Switch (seminar). Software Package for Designing of Clos
	Switches
4	Software Package for the Simulation of Rectangular Delta Switches. Software Package for the Simulation
	of Rectangular Omega Switches
5	Multiple Stage Switches with Multiple Paths (Benes) (seminar). Software Package for the Simulation of
	Benes Switches.
6	STS Switch (seminar). Software Package for the Designing of STS Switches
7	TST Switch (seminar). Software Package for the Designing of TST Switches
8	Software Package for the Calculation of Binomial Bernoulli, Normal Laplace-Gauss and Poisson
	Distributions. Software Package for the Calculation of Erlang B and Erlang C Formulas
9	Queuing systems design: $M/M/1/\infty$, $M/M/1/N$, $M/M/m/\infty$ (seminar)
10	Queuing systems design: $M/M/m/m$, $M/D/1/\infty$, $M/G/1/\infty$ (seminar)
11	Software Package for Bellman-Ford Algorithm
12	Software Package for Dijkstra's Algorithm
13	Synthesis problems covering all chapters (Digital Switching, Queueing Systems, Routing Basics)
14	Recovered laboratories
B. A	pplications – Project (project contents)
1	IPv4 Addressing
2	NGN. Configuration of IP PBX Asterisk using Linux Live distributions (AmatixInstantPBX, AstLinux, ST-
	PBX Live, Slast, CosmoPBX, Medianix, Xorcom_live, AdminsParadise_voip_livecd)
3-5	Implementation of a dial plan for Asterisk with minimum two SIP clients and two IAX clients
	Implementation of common functions: DIAL, RINGING, ANSWER, HANGUP. Implementation of two
	particular functions: VOICE-MAIL, CONFERENCING, SAY, PLAY, WAIT, AUTHENTICATE,
	TIMEOUT, RECORD.
6	Project Recovery
7	Defending the projects.

C. Individual study (reference study contents, synthesis materials, projects, applications etc.)								
3 sets of problems (course homework): Efficiency of multi-stage space division switches, Relation between								
Erlang B and Erlang C Formula, implementation of $M/D/m/\infty$								
Course	Problem	Applications	Examination	Additional	Total no. of individual study			
study	solving,	preparation	time	reference	hours			
	laboratory,			study				
	project			-				
28	22	18	3	9	80			
	blems (course l Erlang C Fo Course study	blems (course homework): l Erlang C Formula, imple Course Problem study solving, laboratory, project	blems (course homework): Efficiency of Erlang C Formula, implementation of Course Problem Applications study solving, preparation laboratory, project	blems (course homework): Efficiency of multi-stage splending C Formula, implementation of M/D/m/∞ Course Problem Applications Examination study solving, laboratory, project	blems (course homework): Efficiency of multi-stage space division switched in the stage space divisin the stage space din the stage space division switched in the s			

References (Textbooks, courses, laboratory manual, exercise book)

- 1. V.Dobrota, *Retele digitale in telecomunicatii. Volumul 1: Comutatia digitala, Analiza traficului.* Editia a III-a, Editura Mediamira, Cluj-Napoca 2002
- 2. V.Dobrota, *Retele digitale in telecomunicatii. Volumul 3: OSI si TCP/IP*. Editia a II-a, Editura Mediamira, Cluj-Napoca 2003
- 3. J.Bellamy, Digital Telephony, John Wiley&Sons, 1991
- 4. D.Bertsekas, R.Gallager, *Data Networks*. Second Edition, Prentice Hall, 1992
- 5. P.Van Mieghem, Performance Analysis of Communications Networks and Systems, Cambridge Univ., 2006
- J.Van Meggelen, L.Madsen & J.Smith Asterisk[™] The Future of Telephony, 2nd Edition, O'Reilly Media Inc, 2007

On-line References

1. V.Dobrota – Switching and Routing Systems, TUCN 2009, <u>http://el.el.obs.utcluj.ro/scr/en_index.htm</u>

Final evaluation	
Evaluation method	Exam including 2 parts (theory + applications): a test (T) with 9 questions from course and
	laboratory (1 hour) and a synthesis problem (P2) covering all chapters (1 hour). The mark
	obtained for the project (P1) represents 50% of the mark for applications.
Mark components	Test (T)= 010 p, Problems P=P1+P2=010 p, Problem P1=project=05 p, Problem
	P2=05 p.
Mark computation	$N=(T+P)/2$, the credits are obtained if N \geq 5; T \geq 5; P \geq 4.5 (P1 \geq 2.5, P2 \geq 2)
Course leader,	

Professor Virgil DOBROTA, Ph.D.