



## SYLLABUS

1. Data about the program of study

Institution	The Technical University of Cluj-Napoca		
Faculty	Electronics, Telecommunications and Information		
T acuity	Technology		
1.3 Department Applied Electronics			
1.4         Field of study         Electronics and Telecommunications Engineering			
Cycle of study	Bachelor of Science		
Program of study/Qualification	Telecommunications Technologies and Systems/		
Frogram of study/Qualification	Engineer, Applied Electronics/ Engineer		
Form of education	Full time		
Subject code	TST-E20.00, EA-E20.00		
	Faculty Department Field of study Cycle of study Program of study/Qualification Form of education		

## 2. Data about the subject

2.1	Subject name	Materials for Electronics					
2.2	Subject area	Electronics and Telecommunications Engineering					
2.3	Course responsible/lecturer	Assoc. Prof. Cristian Farcas, PhD					
2.4	Teachers in charge of applications	Assoc. Prof. Niculaie Palaghita, PhD					
		Assistant Ionut Ciocan, PhD					
2.5	Year of study II 2.6 Semester 1	1 2.7 Assessment V 2.8 Subject category DID/DOE					

### 3. Estimated total time

Year/	Subject name	No.	Course	App	licatio	ons	Course	App	olicati	ons	Indiv.		
Sem.		of							study	-AL	dits		
		weeks	[hours/week]		[hours/sem.]				<u>-</u>	Credits			
				S	L	Ρ		S	L	Ρ			0
II / 1	Materials for Electronics	14	2		1		28		14		62	104	4

3.1	Number of hours per week	3	3.2	of which, course	2	2	applications	1		
3.4	Total hours in the curriculum	42	3.5	of which, course	28	3.6	applications	14		
Individual study										
Manual, lecture material and notes, bibliography										
Supplementary study in the library, online and in the field										
Preparation for seminars/laboratory works, homework, reports, portfolios, essays										
Tutoring										
Exams and tests										
Othe	r activities							0		
3.7	Total hours of individual study		62							

3.7	I otal hours of individual study	62
3.8	Total hours per semester	104
3.9	Number of credit points	4

## 4. Pre-requisites (where appropriate)

4.1	Curriculum	N/A
4.2	Competence	Relations and theorems for electric circuits; physics; chemistry;

## 5. Requirements (where appropriate)

5.1	For the course	Amphitheatre, Cluj-Napoca
5.2	For the applications	Laboratory, Cluj-Napoca

## 6. Specific competences

		The student must know:
	MOI	✓ Matter structure and lattice defects;
	ge t kr	✓ classification of materials - conductors, insulators, semiconductors;
	led	✓ properties of dielectrics and their applications;
	nt r	✓ breakdown in dielectrics;
	h hde	<ul> <li>✓ classification of semiconductors;</li> </ul>
	ical stu	✓ intrinsic and extrinsic conduction;
	the	✓ properties and applications of semiconductors;
	Theoretical knowledge (what the student must know):	<ul> <li>✓ electrical conductions in metals;</li> </ul>
	たい	✓ properties of magnetic materials and their applications;
	∷ at	After completing the discipline, the students will be able to:
	(what do):	✓ recognize different types of materials;
	e to	<ul> <li>✓ determine different parameters of capacitors;</li> </ul>
ses	skills s able	<ul> <li>✓ distinguish different types of magnetic materials;</li> </ul>
enc	tis :	<ul> <li>✓ determine different parameters of a conductive materials;</li> </ul>
pet	ed	<ul> <li>✓ use pn junction as temperature sensor;</li> </ul>
lmo	quir	
Professional competences	Acquired skills (wha the student is able to do):	
ona		After completing the discipline, the students will be able to:
ssi	Acquired abilities: (what type of equipment the student is able to handle)	$\checkmark$ use the lab instrumentation (power supply, oscilloscope, function generator,
ofe	: (v the and	multimeter) for the experimental studies;
Ъ	ies ent t o h	<ul> <li>✓ determine relative dielectric constant;</li> </ul>
	bilit ome ole t	<ul> <li>✓ display on an oscilloscope the hysteresis curve of a magnetic core;</li> </ul>
	d al quir sat	<ul> <li>display on an oscilloscope the hysteresis curve of a magnetic core;</li> <li>display the voltage vs current characteristic of electronic devices;</li> </ul>
	nt is int is	
	Acquired abilities: (wha type of equipment the student is able to handle)	
	st A	C1. To use the fundamental elemente regarding electronic devises circuite evisteme
	<u>د</u>	C1 - To use the fundamental elements regarding electronic devices, circuits, systems,
	wit ila2	instrumentation and technology
	s Gr	C4 - To design and use low complexity hardware and software applications, specific to applied electronics
	ordance 1 and G RNCIS	C5 - To apply knowledge, concepts and basic methods from power electronics, automated
	a1 a RN	systems, electric energy management, electromagnetic compatibility
	n accordance with Grila1 and Grila2 RNCIS	C6 - To solve technological problems, specific to applied electronics
	<u> </u>	co - To solve technological problems, specific to applied electronics
		N.A.
	) nd	N.A.
SS	petenc rila1 ar Grila2 RNCIS)	
Cross	mpetences (Grila1 and Grila2 RNCIS)	
	competences (Grila1 and Grila2 RNCIS)	
	о О	

### 7. Discipline objectives (as results from the key competences gained)

7.1	General objectives	Developing the competences regarding the use and analysis of electronic materials.
7.2	Specific objectives	<ol> <li>Developing skills and abilities necessary for the use of the electronic materials.</li> <li>Developing skills and abilities for the use of the laboratory equipment.</li> </ol>

#### 8. Contents

8.1.	Lecture (syllabus)	Teaching methods	Notes				
1	Course description. An overview of electronic materials.	, L					
2	Matter structure and bonding	tior	ior,				
3	Electronic band theory of solids	nta idy	ect				
4	Classification of materials - conductors, insulators, semiconductors	n, stu n	loj				
5	Dielectric materials – definitions, classifications and general aspects	, atic se atio	ů, p				
6	Fundamental properties of dielectrics	ers: ers: ca	ard				
7	Applications of dielectrics	ntat nve ble se,	nta				
8	Breakdown of dielectrics. Dielectric materials used in electronics.	ser co pro rci	ese				
9	Semiconductor materials – definitions, classifications and general aspects	Presentation, heuristic conversation, exemplification, problem presentation, teaching exercise, case study, formative evaluation	.ppt presentation, projector, blackboard				
10	Intrinsic semiconductors	fo the fice					
11	Extrinsic semiconductors	ipli eac	e of				
12	PN junction. Some semiconductors used in electronics.	ten	Use of				
13	Conductors	ê					
14	Magnetic materials. Preparation for the final exam.						
8.2.	Applications (lab)	Teaching methods	Notes				
1	Introduction. Labour protection	ſ	_				
2	Electrical conductor materials	of, ean	∕ ds, arc				
3	Ferromagnetic materials	d roc , té	bo, bo				
4	Solid dielectric materials	an al p iise	bc titic tic				
5	P-N junction barrier capacitance	actic a nenta exerci work	abc ent ital				
6	Temperature dependence of resistivity (conductors and semiconductors)	Didactic and erimental pr tic exercise, work	Use of laboratory instrumentation, experimental boards, white/ magnetic board				
7	Lab recovery and finalization of laboratory activity	ctrical conductor materials     protoctation       comagnetic materials     dielectric materials       junction barrier capacitance     junction barrier capacitance       nperature dependence of resistivity (conductors and niconductors)     protoctation       recovery and finalization of laboratory activity     protoctation					

Bibliography

1. Fărcaș Cristian – Materiale pentru electronică, Ed. Risoprint, Cluj-Napoca, 2009

Cret Rodica - Materiale pentru electronică, U.T. Press, Cluj-Napoca, 2004

2. 3. Pitică Dan, Radu Mihaela - Componente electronice pasive, Litografia UTC-N, 1994

4. Schroder D. – Semiconductor Material and Device Characterization, John Wiley & Sons, 2006

5. Yu P., Cardona M. – Fundamentals of Semiconductors. Physics and Materials Properties, Springer, 2010.

- 6. Pop V., Chicinaş, Jumate N. Fizica materialelor. Metode experimentale, Presa Universitară Clujeană, 2001
- Drăgulinescu M., Manea, A., Materiale pentru electronică, Ed. Matrix Rom, București, 2002. 7.

8. Notingher, P., Materiale pentru electrotehnică, Ed. Politehnica Press, București, 2005.

9. Popovic, R.S., Hall Effect Devices - 2nd ed., Bristol; Philadelphia: Institute of Physics, 2004.

10. Zeghbroeck, B., Principles of Semiconductor Devices and Heterojunctions, Paperback - Nov 25, 2008.

# 9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field

Competences acquired will be used in the following COR occupations (Electronics Engineer; Telecommunications Engineer; Electronics Design Engineer; System and Computer Design Engineer; Communications Design Engineer) or in the new occupations proposed to be included in COR (Sale Support Engineer; Multimedia Applications Developer; Network Engineer; Communications Systems Test Engineer; Project Manager; Traffic Engineer; Communications Systems Consultant).

### 10. Evaluations

Activity type	10.1	Assessment criteria	10.2	Assessment methods	10.3	Weight in the final	
						grade	
Course		The level of acquired		<ul> <li>Summative evaluation</li> </ul>		- E, max 10 pts.	
		theoretical knowledge and		written exam (theory		80%	
		practical skills		and problems)			
Applications		The level of acquired abilities		- Continuous formative			
				evaluation		- L, max. 10 pts.	
				<ul> <li>practical lab test</li> </ul>		20%	
10.4 Minimum standard of performance							
L ≥ 5 and E ≥ 4.5 and 0,8E+0,2L≥ 4.5							

Date of filling in 29.01.2015

Course responsible Assoc. Prof. Cristian Fărcaş, PhD Teachers in charge of applications Assoc. Prof. Niculaie Palaghita, PhD Assist. Ionuţ Ciocan, PhD

Date of approval in the department 29.01.2015

Head of department Prof. Dorin Petreuş, PhD