



Alin-Tudor Sferle

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WORK EXPERIENCE

01/02/2022 – CURRENT Cluj-Napoca, Romania

SYSTEM DESIGN ENGINEER ANALOG DEVICES

- Developing HDL IP cores for interfacing FPGAs with our group's evaluation boards (DAS and Ethernet PHY's evaluation boards);
- Verification of RTL designs;
- Linux kernel drivers' development [iio, pwm].
- FPGA-based reference designs for GMSL (Gigabit Multimedia Serial Link) devices.

15/02/2021 – 31/01/2022 Cluj-Napoca, Romania

SYSTEM DESIGN ENGINEER INTERN ANALOG DEVICES

- Developing HDL IP cores for interfacing FPGAs with our group's evaluation boards (DAS and Ethernet PHY's evaluation boards);
- Formal verification of RTL designs;

01/10/2020 – 01/12/2020 Cluj-Napoca, Romania

STUDENT MENTOR TECHNICAL UNIVERSITY OF CLUJ-NAPOCA

- Organizing mentoring sessions to support international students from Technical University of Cluj-Napoca to acquire necessary information to be able to deal with hard subjects;

01/08/2020 – 10/09/2020 Cluj-Napoca, Romania

STUDENT INTERN TECHNICAL UNIVERSITY OF CLUJ-NAPOCA

- Build an educational bot with QnA Maker and Azure Bot Service;
- Microsoft 365 Fundamentals, Azure Cloud Fundamentals, Azure IaaS, Azure Networking Fundamentals, Azure PaaS, Serverless Computing;
- Write an article in IEEE template, on the topic: "Educational technologies in the distance – learning era" about implementation of Moodle Platform in three different universities;

EDUCATION AND TRAINING

01/10/2018 – 07/07/2022 Cluj-Napoca, Romania

BACHELOR'S DEGREE IN TELECOMMUNICATIONS ENGINEERING Faculty of Electronics, Telecommunications and Information Technology

Website etti.utcluj.ro | Thesis Kubernetes as a Service solution for private cloud infrastructure

01/10/2022 – CURRENT Cluj-Napoca, Romania

MASTER'S DEGREE IN TELECOMMUNICATIONS ENGINEERING Technical University of Cluj-Napoca

- PatriotFest 2023 + SSET 2024 contests – "AIGenPhishing_Consultant" idea, that exposes a new approach of a fast-to-use & fast-to-develop solution for self-security against AI-generated phishing tentative – bot approach using Microsoft Bot Framework & Azure Bot Service for Microsoft Teams app.
- B.S. Thesis consultant - Denisa-Oana Selin, "WebRTC and SIP using Asterisk in the Cloud", Thesis Advisor - Professor, Ph.D., MIEEE Virgil Dobrota.

Website <https://etti.utcluj.ro>

● HONOURS AND AWARDS

20/05/2022

Honorable Mention – Technical University of Cluj-Napoca

SSET 2022

Paper: Microservices-Based Web Application for Managing Kubernetes Clusters

4th Prize – SAS TUCN

4th prize at Demo Day Competition organized by Student Entrepreneurial Society from Technical University of Cluj-Napoca with idea of implementing intelligent chatbots at every university in order to help first year students to find all necessary information quicker;

07/11/2020

3rd Prize – Military Technical Academy "Ferdinand I" Bucharest

STUDENTS' INTERNATIONAL CONFERENCE "CERC" 2020

Paper: "WayToTUCN, an Educational Bot with QnA Maker and Azure Bot Service"

Link https://www.mta.ro/international/wp-content/uploads/2020/11/Awards_CERC2020.pdf

01/06/2020

VHDL to Embedded Linux Course – Digilent Romania

VHDL Club Digilent Romania | February 2020-June 2020 | VHDL to Embedded Linux Course on Digilent FPGAs | Projects: Olympic Circles displayed on Full HD Screen using Basys 3 FPGA;

● NETWORKS AND MEMBERSHIPS

10/01/2021 – 01/07/2022

Microsoft Learn Student Ambassador

● VOLUNTEERING

01/03/2016 – CURRENT

Google Local Guide

01/06/2018 – CURRENT

Google Crowdsourcing Contributor

● PUBLICATIONS

2023

[Work-in-Progress: KubeUT: A Cloud-Agnostic Kubernetes Management Platform](#)

Robert Botez, Alin-Tudor Sferle, Iustin-Alexandru Ivanciu, Virgil Dobrota

2023

[Efficient Network Slicing with SDN and Heuristic Algorithm for Low Latency Services in 5G/B5G Networks](#)

Robert Botez, Andres-Gabriel Pasca, Alin-Tudor Sferle, Iustin-Alexandru Ivanciu, Virgil Dobrota

2024

[WebRTC and PJSIP in Asterisk: A Cloud-Based Approach](#)

Denisa-Oana Selin, Alin-Tudor Sferle, Tudor-Mihai Blaga, Daniel Zinca, Virgil Dobrota