

## SYLLABUS

### 1. Data about the program of study

1.1 Institution	Technical University of Cluj-Napoca
1.2 Faculty	Faculty of Electronics, Telecommunications and Information Technology
1.3 Department	Mathematics
1.4 Field of study	Electronic Engineering, Telecommunications and Information Technologies
1.5 Cycle of study	Bachelor of Science
1.6 Program of study / Qualification	Telecommunications Technologies and Systems/ Engineer Applied Electronics/Engineer
1.7 Form of education	Full time
1.8 Subject code	TST-E12.00/EA-E12.00

### 2. Data about the subject

2.1 Subject name	Electronic devices						
2.2 Subject area	Electronic devices and circuits						
2.3 Course responsible/lecturer	Assist.Prof. Laura-Nicoleta IVANCIU, Ph.D <a href="mailto:laura.ivanciu@bel.utcluj.ro">laura.ivanciu@bel.utcluj.ro</a>						
2.4 Teachers in charge of applications	Assist.Prof. Laura-Nicoleta IVANCIU, Ph.D <a href="mailto:laura.ivanciu@bel.utcluj.ro">laura.ivanciu@bel.utcluj.ro</a> Assist.Prof. Emilia SIPOS, Ph.D <a href="mailto:emilia.sipos@bel.utcluj.ro">emilia.sipos@bel.utcluj.ro</a>						
2.5 Year of study	1	2.6 Semester	2	2.7 Assessment	E	2.8 Subject category	DD/DI

### 3. Estimated total time

3.1 Number of hours per week	4	of which : 3.2 course	2	3.3 seminar / laboratory	2
3.4 Total hours in the curriculum	56	of which: 3.5 course	28	3.6 seminar / laboratory	28
Distribution of time					hours
Manual, lecture material and notes, bibliography					23
Supplementary study in the library, online specialized platforms and in the field					-
Preparation for seminars / laboratories, homework, reports, portfolios and essays					40
Tutoring					3
Exams and tests					3
Other activities: .....					
3.7 Total hours of individual study	69				
3.8 Total hours per semester	125				
3.9 Number of credit points	5				

### 4. Pre-requisites (where appropriate)

4.1 Curriculum	Passive Components and Circuits, Physics
4.2 Competence	Electrical signals, connection of passive components, relations and theorems for electric circuits, time and frequency behavior of capacitors and inductors, frequency response representation.

### 5. Requirements (where appropriate)

5.1. For the course	Amphitheater, Cluj-Napoca
5.2. For the laboratories	Laboratory, Cluj-Napoca

### 6. Specific competences

<b>Professional competences</b>	<p>C1. Use of the fundamental elements related to devices, circuits, systems, instrumentation and electronic technology</p> <p>C2. Applying the basic methods for the acquisition and processing of signals</p> <p>C4. Design, implementation and operation of data, voice, video and multimedia services. This is based on the understanding and the application of fundamental concepts in telecommunications and transmission of information</p> <p>C5. Selecting, installing, configuring and operating fixed or mobile telecommunications equipment. Equipping a site with usual telecommunications networks</p>
<b>Transversal competences</b>	N/A

### 7. Discipline objectives (as results from the key competences gained)

7.1 General objectives	Developing the competences regarding the use of electronic devices.
7.2 Specific objectives	<ol style="list-style-type: none"> <li>1. Recognizing and understanding basic concepts specific to electronic devices.</li> <li>2. Developing skills and abilities necessary for the use of electronic devices in simple electronic circuits</li> <li>3. Developing skills and abilities for the analysis and (re)design of electronic circuits.</li> </ol>

### 8. Contents

8.1 Lecture (syllabus)	Teaching methods	Notes
1. Presentation of course structure. Review: electrical signals, relations and theorems for electric circuits, RC circuits, frequency response representation	Presentation, heuristic conversation, exemplification, problem presentation, teaching exercise, case study, formative evaluation	.Use of .ppt presentation, projector, blackboard
2. Diodes. Models for switching diode. DR circuits.		
3. DR switching circuits. Switching DC circuits. Single-phase rectifiers with capacitive filter.		
4. Full-wave DR rectifiers. DC switching circuits. DRC rectifiers. LEDs.		
5. Zeener diodes. Operational amplifiers (OpAmps). OpAmp operation. Ideal OpAmp. Modes of use.		
6. Simple op-amp comparators. Inverting and noninverting comparators. Voltage transfer characteristic. Waveforms.		
7. Positive feedback OpAmp comparators. Inverting and noninverting comparators. Voltage transfer characteristic. Waveforms.		
8. Electronic amplifiers: definition, power supply, voltage transfer characteristic, modeling, performance evaluation. Negative feedback op-amp amplifiers. Non-inverting and inverting amplifier.		

9. Summing amplifiers. Differential amplifiers.		
10. Applications with OpAmp: voltage domain conversion circuits, capacitively coupled amplifiers, amplifiers operated from a single power supply, integrators and differentiators.		
11. Transistors. Types. Operating principle and operating regions. Use in circuits. Transfer characteristics. BJTs: symbol, internal structure		
12. BJTs operating principle and equations, transistor characteristics, operating regions, saturation. Switching MOS transistor: analog switch, CMOS inverter. Noise margins.		
13. MOS transistors: symbol, physical structure, operating principle and equations, static characteristics, operating regions.		
14. Recapitulation. Preparation for the final exam.		
<b>8.2 Laboratory</b>	Teaching methods	Notes
1. Introduction. Workplace safety.	Didactic and experimental proof, didactic exercise, team work	Use of laboratory instrumentation, experimental boards, computers, smart board
2. Lab instrumentation. Voltage divider.		
3. Semiconductor diodes		
4. DR switching circuits, two-port and multi-port networks		
5. DC switching two-port network		
6. Single phase rectifiers with capacitive filter		
7. Circuits with Zener diodes and LEDs.		
8. Voltage comparator with op-amp - simple comparators		
9. Optical indicator for voltage level with OpAmp		
10. Voltage comparator with op-amp - hysteresis comparators		
11. Basic amplifiers with OpAmp		
12. Rail-to-rail OpAmp amplifier with unipolar supply		
13. Laboratory test		
14. Lab do-overs and finalization of lab activity		
<b>Bibliography</b> <b>On-line references</b> <ol style="list-style-type: none"> <li>Ivanciu, Laura-Nicoleta. Electronic devices (course slides, laboratories, problem examples, exam subjects), <a href="http://www.bel.utcluj.ro/dce/didactic/ed/ed.htm">http://www.bel.utcluj.ro/dce/didactic/ed/ed.htm</a></li> <li>Sipos, Emilia, Ivanciu, Laura, Dispozitive Electronice. Probleme rezolvate, 2016</li> </ol> <b>Offline references</b> <ol style="list-style-type: none"> <li>Oltean, G., Electronic Devices, Editura U.T. Pres, Cluj-Napoca, ISBN 973-662-220-7, 2006; 317 pag.</li> <li>Oltean, G., Sipos, Emilia, Miron, C., Ivanciu, Laura, Laboratory Manual for Electronic Devices, Editura UTPRESS, Cluj Napoca, 2010, ISBN 978-973-662-542-8, 90 pag.</li> </ol>		

### 9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field

The discipline content and the acquired skills are in agreement with the expectations of the professional Competences acquired will be used in the following COR occupations (Electronics Engineer; Telecommunications Engineer; Electronics Design Engineer; System and Computer Design Engineer; Communications Design Engineer) or in the new occupations proposed to be included in COR (Sale Support Engineer; Multimedia Applications Developer; Network Engineer; Communications Systems Test Engineer; Project Manager; Traffic Engineer; Communications Systems Consultant).

## 10. Evaluations

Activity type	10.1 Assessment criteria	10.2 Assessment methods	10.3 Weight in the final grade
10.4 Course	The level of acquired theoretical knowledge and practical skills	- 10 homework activities - optional (problem solving) - Summative evaluation written exam (problem solving)	- H, max 10 pts, 10% - E, max 10 pts 70%
10.5 Applications	The level of acquired abilities	- Continuous formative evaluation - Laboratory test (practical evaluation)	- L, max. 10 pts, 30%
<b>10.6 Minimum standard of performance</b>			
Qualitative level: <ol style="list-style-type: none"> <li>To recognize and understand basic concepts specific to electronic devices.</li> <li>To develop skills and abilities necessary for the use of electronic devices in simple electronic circuits</li> <li>To analyze and (re)design electronic circuits.</li> </ol> Quantitative level: <ol style="list-style-type: none"> <li>Full laboratory attendance</li> <li>Final grade computed as: <math>\min(10, 0,7E+0,3L+0,1H) \geq 4.5</math>, where <math>L \geq 5</math> and <math>E \geq 4</math>.</li> </ol>			

Date of filling in:	Responsible	Title Surname NAME	Signature
27.09.2021	Course	Assist.Prof. Laura-Nicoleta IVANCIU, Ph.D	
	Applications	Assist.Prof. Laura-Nicoleta IVANCIU, Ph.D	
		Assist.Prof. Emilia SIPOS, Ph.D	

Date of approval in the Department of Communications 27.09.2021	Head of Communications Department Prof. Virgil DOBROTA, Ph.D.
Date of approval in the Council of Faculty of Electronics, Telecommunications and Information Technology 27.09.2021	Dean Prof. Gabriel OLTEAN, Ph.D.